		lesson Plan			
Name of th	ne Faculty	: Ms. Suman Bala			
Discipline	-	: Electronics and Communication Engg.			
Semester		: IVth			
Subject		: NETWORK FILTERS AND TRANSMISSION LINES			
Lesson Pla	In Duration	: Jan-Apr-2018			
Work Load	l (Lecture/P	Practical) per week (In hours): Lect 03, Practical - 03			
Week		Theory		Practical	
	Locturo				
	Lecture	Topic (including assignment/test)	Practical Day	Topic	
. ct	uay		,		
151	1	Introduction to network system	1.04		
	2	Networks : I wo port or four terminals network, stubs	IST	Introduction about Practical	
	3	Basic concepts of Symmetrical and asymmetrical networks ,Balanced and		F U U U U U	
	4	Basic concepts of 1-network, - Network, Ladder network,Lattice network,L-	Que el	To measure the characteristic	
2nd	5	Concept and significance of the terms characteristic impedance, propagation	200	impedance of symmetrical 1 and	
	6	Characteristic impedance of a - Network		- Network	
	/	Characteristic impedance of a - Network		To measure the image	
3rd	8	Propagation constant of a 1- Network	310	Impedance of a given	
	9	Propagation constant of a - Network		asymmetrical I and - Network	
	10	Attenuation and phase constant of a Land - Network	1+1-	To determine the characteristic	
4th	11	Insertion loss of 1-network and Network	411	impedance experimentally of a	
	12	Revision of unit- I		prototype low pass filter and plot	
	13	Asymmetrical Network Concept and significance of iterative impedance	E t la	To design and measure the	
5th	14	Image Impedance, Image transfer constant and insertion loss	5th 8	attenuation of a symmetrical 17	
	15	The half section (L-section); symmetrical T and sections into half sections		type attenuator	
	16	lest	(+ 1-	To determine the characteristic	
₆ th	17	Attenuators, Units of attenuation ,General	6th	impedance experimentally of a	
	18	Analysis and design of Symmetrical Lattenuator		prototype high pass filter and	
	19	Analysis and design of Symmetrical attenuator	7th	To determine the impedance	
7th	20	Analysis and design of L- type attenuator			
	21	Numerical problem on design of attenuators		characteristic of a prototype band	
415	22	Fillers Brief idea of the use of filter naturalis in different communication systems	0th	To determine the impedance	
810	23	Brief idea of the use of filter networks in different communication systems	0111	characteristic and attenuation	
	24	Test of Zhu unit			
. th	25	Dw pass, high pass, band pass and band stop liners.	Oth	sharastoristic and attenuation	
9111	20	Attenuation Vs frequency: Dhase shift Vs frequency sharestoristics	9111	characteristic of a m derived	
	27	Simple design problems of prototype low pass filter		To observe the information of	
th	20	Simple design problems of prototype low pass filters, need of m derived	10th	standing wayes on a transmission	
10	29	Crystal Filters Crystal and its equivalent circuits, special properties of	TOUT	line and measurement of SWP	
th.	21	Active Filters Pasic concent of active filters and their comparison with passive		Draw the attenuation	
11	20	Active Filters basic concept of active filters and their comparison with passive	11th		
	22	Transmission Lines Transmission Lines, their types and applications			
th	24	Distributed constants. T and consecutation of transmission line section			
12	25	Secondary constants, 1 and Teplesentation of italismission line equation.	12th	Popoat of any experiment	
	35	transmission line equation infinite line concept	1211	Repeat of any experiment	
th	27	Pivision and Test of transmission line			
13	20	Condition for minimum distortion and minimum attonuation of signal on the	13th	Viva	
	20	Concept of reflection and standing waves definition of reflection coefficient	1501	VIVG	
th	40	Concept of reflection and standing waves, definition of reflection coefficient,			
14	40 ⊿1	Impedence matching, quarter wave transformer	1/th	Repeat of any other experiment	
	41	Indroduction to stube, single, open short stubs	1401	Repeat of any other experiment	
th	42	Rivision of 4th unit			
15	43	Test	15th	Viva	
1	44	1030	15111	VIVG	

45

Revision

Lesson Plan

Name of the Faculty : Yet to Be Assigned Discipline : Electronics and Communication Engg.

Semester : 1vth

Subject : MICROPROCESSORS AND PERIPHERAL DEVICE

Lesson Plan Duration : from jan 2018-April 2018

Work Load (Lecture/ Practical) per week (in hours): Lecture=04, Practical-02

Week		Theory		Practical	
	Lecture day	Topic (including assignment/ test)	Practical Day	Торіс	
	1 st	Introduction to microprocessor			
	2nd	Evolution of Microprocessor			
1st	3rd	Organization of a microcomputer and functions of its various blocks		Introduction about Practical	
	4th	Microprocessor, its evolution, function and impact on modern society	1st		
	5th	Architecture of a Microprocessor (8085)			
	6 th	Concept of Bus, bus organization of 8085,		Familiarization of different keys of	
2nd	7th	Functional block diagram of 8085		8085 microprocessor kit and its memory map	
	8th	Revision & Test	2nd		
	9th	8085 pin diagram			
	10 th	Function of various pins	Fami	Familiarization of different keys of	
3rd	11 th	Demultiplexing of address/data bus		8085 microprocessor kit and its memory map	
	12th	Generation of read/write control cycle	3rd		
	13 th	Steps to execute a stored programme	0.4	Steps to enter, modify data/program and to execute a	
	14 th	Instruction cycle		programme on 8085 kit	
4th	15 th	Fetch cycle			
	16 th	machine cycle			
	17 th	T-states		Writing and execution of ALP for	
41-	18 th	Fetch cycle, execute cycle		bit numbers	
501	19 th	Data transfer instruction			
	20 th	Data transfer instruction	5th		
	21st	Arithmetic instruction part 1		Writing and execution of ALP for multiplication and division of two	
	22 nd	Arithmetic instruction part 2		8 bit numbers	
6th	23 rd	Logical instruction part 1			
	24 th	Logocal instruction part 2	6th		
	25 th	Revision of data transfer instruction		Writing and execution of ALP for arranging 10 numbers in	
	26 th	Branch instruction		ascending/descending order	
7th	27th	Revision of arithmetic instruction			
	28th	Stack instruction	7th		

	29 th	I/O instruction		Writing and execution of ALP for 0 to 9 BCD counters (up/down
	30 th	Machine control instruction programming in assembly language		counter according to choice stored in memory)
8th	31 st	Concept of memory mapping		y,
	32 nd	Partitioning of total memory space		
	33 rd	Address decoding	0111	
	34 th	Concept of memory mapping		Interfacing exercise on 8255 like
9th	35 th	Partitioning of total memory space		LED display control
	36 th	adder ess	9th	
	37 th	Conept of pheripheral mapped I/O and memory mapped I/O		
	38th	Interfacing of memory mapped I/O devices	_	Interfacing exercise on 9252
10 th	39 th	Interrupts, Maskable and non-maskable		programmable interval timer
	40 th	Edge triggered and level triggered interrupt	_	
	41 st	Software interrupt, Restart interrupts and its use	10th	Interfacing exercise on 8279
11th	42 nd	Various hardware	-	programmable KB/display interface like to display the hex
	43 rd	interrupts of 8085 Servicing interrupts	-	code of key pressed on display
	44 th	RIM and SIM instruction	-	
	45 th	Priority interrupt controller	l ltn	
th	46 th	Data Transfer Techniques : sync data transfer, async data transfer (hand shaking),		Use of 8085 emulator for
12	47 th	Interrupt driven data transfer		hardware testing
	48 th	DMA	12th	
	49 th	Serial data transfer	1201	
415	50th	Block diagram of 8255 PPI		
13	51 st	Pin diagram of8255 PPI		Viva
	52nd	Operating modes of 8253 PIT	13th	
	53 rd	8257 / 8237 DMA controller		
th	54 th	8251 Communication Interface Adapter(Block diagram)		
14	55 th	8251 Communication Interface Adapter(Pin diagram)		Viva
	56th	Revision & Test	14th	
	57th	8279 programmable KB/Display Interface(Block diagram)		
	58th	8279 programmable KB/Display Interface(pi n diagram, function of pins)		
₁₅ th	59 th	Revision		Viva
	60 th	Test	15th	

LESSON PLAN

Name:-Discipline:-Semester:-Subject:- Yet To Be Assigned E.C.E 4TH Digital Electronics- II

Lesson plan duration:- 15 Weeks (from jan 2018-April 2018) Work Load (Lecture/Practical) Per Week (in hours) :- Lectures-03, Practical-03

Week	Theory		Practical		
	Lecture Day	Topic(including assignment test)	Practical Day	Торіс	
	1et	Types of integration			
	150	Classification of logic family			
1st	2 nd	Characteristics of logic family	1st	Binary resistor n/w DAC	
	3rd	Operation of RTL, TTL, ECL logic family			
	4th	Operation of CMOS, NAND, NOR gates			
Quart	541	Comparison of TTL and CMOS	Que el	Desistence la dalar a (o. DAO	
2na	Sth	CMOS to TTL and TTL to CMOS interfacing	2na	Resistance ladder n/w DAC	
	6th	Tristate device			
	741	Revision			
	7th	Assignment Unit-1			
2rd	Q41.		- Drd		
310	oui	Piperu register p/w DAC	Siù	viva-voce	
	9th	binary resistor n/w DAC			
		Resistance ladder n/w DAC			
	10th	Performance characteristics of DAC			
		Single slope ADC			
4th	11th	Dual slope ADC	4th	ADC	
	12th	Successive Approximation ADC.			
		Parallel ADC.			
	13th	Performance characteristics of DAC			
5th	14th	Revision	5th	Eull Addor Ckt	
511	1401	Assignment Unit-2	501		
	15th	Test Unit-2			
	16th	Memory organisation			
6th	17th	Classification of Semiconductor memory	6th	Viva-voce	
oth	18th	Random Access memory.	otti	VIVA-VOCC	
		Read only memory.			
	19th	Expension of memory			
7th	20th	CCD.	7th	Full Subtractor Ckt	
	2011	CAM.	7.01		
	21st	Programmable logic arrey.	1		
	22nd	Programmable logic arrey.	j		
		FPGA.			
8th	23rd	Familiarization with comman ICs.	8th	Design J-Kflip Flop counter.	
	24th	Revision			
		Assignmnt Unit-3			

	25th	Test			
9th	26th	K-Map using 5 & 6 Variables.	9th	Viva-Voce	
	27th	Quine Mccluskey method.			
	28th	Problem sloving .			
10th	29th	Test Unit-4	10th	Logical operation using IC 741	
	30th	Sequential cks.			
	31st	Synchronous sequential ckts. Asynchronous sequential ckts.			
11th	32nd	Mealy and moore m/c	11th	Arithmatic operation using IC	
	33rd	State Diagram. Excitiation table		74101	
	34th	Designing of counters.			
1044	35th	Practice of designing of counters.	1044		
12th	36th	Revision Assignment Unit-5	TZIN	viva-vole	
	37th	Test			
12+b	38th	Arithmetic logic unit IC74181	12th	Mriting operation of DAM IC	
1311	39th	Implementation of Binary arithmetic Multiplication Division Subtraction Addition	1501	writing operation of RAM IC.	
	40th	Revision Assignment Unit-6			
14th	41st	Test	14th	Reading operation of RAM IC	
	42nd	Fuzzy and Classical sets Operation on sets			
15th	43rd	Fuzzy relation Membership function			
	44th	Fuzzification Defuzzification Fuzzy control system	15th Viva-Voce		
	45th	Test			

Lesson Plan

Name of the Faculty	: Yet To Be Assigned			
Discipline	: Electronics and Communication En			
Semester	: IVth			
Subject	: Instrumentation			
Lesson Plan Duration	: Jan-Apr-2018			

Work Load (Lecture/ Practical) per week (in hours): 03 HOURS (Lecture)03 Hours per Group (PRACTICAL)

Week		Theory	Practical	
	Lecture day	Topic (including assignment/ test)	Practical Day	Торіс
1st	1	Introduction about subject		
	2	Measurements: Importance of measurement, basic measuring systems.	1st	Introduction about Practical Instrumentation
	3	Advantages and limitations of each measuring system.		
2nd	4	Display devices		
	5	Theory of Transducers: construction and use of various transducers	2nd	To measure the level of a liquid using a transducer
	6	Resistive transducers and wire wound potentiometer.		
3rd	7	Capacitive transducers		
	8	Inductive transducers	3rd	To measure temperature using a thermo-couple
	9	Electromagnetic, piezo electric type transducer.		
4th	10	Measurement of Displacement and Strain: LVDT and RVDT transducer.		
	11	Strain gauges and Gauge factor, gauge materials and their selections.	4th	Study and use of digital temperature controller
	12	Use of electrical strain gauges their different types such as inductance type resistive type, wire and foil type etc.		
5th	13	Strain gauge bridges and amplifiers.		
	14	Revision	5th	Use of themistor in ON/OFF transducer
	15	Revision		
6th	16	Test & Assignment		
	17	Force Measurement: Different types of force measuring devices and their principles	6th	Study of variable capacitive transducer
	18	Load cells		
7th	19	load measurements by using elastic transducers and electrical strain gauges.	7.45	
	20	Torque Measurement: Different types of torque measurement methods.	7th	Draw the characteristics of a potentiometer
	21	Measurements of torque by brake and dynamometer.		
8th	22	Speed measurements; different methods, devices.		
	23	Pressure Measurement	8th	To measure linear displacement using LVDT
	24	Bourdon pressure gauges	1	

9th	25	Electrical pressure pickups and their principle construction and applications.	0.41	To study the use of electrical strain gauge
	26	Low pressure measurements and Use of pressure cells.	9th	
	27	Revision		
1011	28	Revision		
TUth	29	Test & Assignment	10th	To study weighing machine using load cell
	30	Flow Measurement: Basic principles of magnetic flow meters.	-	
	31	Ultrasonic flow meters		
11th	32	Measurement of Temperature: Bimetallic thermometer.	11th	To study pH meter.
	33	Resistance thermometers		
	34	Thermisters		
12th	35	Thermocouple	12th	Revision & Viva
	36	Pyrometer	1	
	37	Temperature recorders		
13th	38	Measurement of other non electrical quantities such as humidity	13th	Revision & Viva
	39	pH value measurements		
	40	Level measurements		
14th	41	Vibrations measurements	14th	Revision & Viva
	42	Revision	1	
	43	Revision		
15th			15th	Revision & Viva
	44	Revision		
	45	Test		

: Mr. Kiran Kumar (Theory, Practical) : (Elex & Comm. Engineering) : 4TH : Communication System : 15 Week (from January, 2018) : Lectures-03, Practicals-03

Week		Theory	Practical		
	Lecture Day	y Topic(including assignment test)	Practical Day	Topic	
1 st	1	Introduction about subject, AM/FM Transmitters , Classification of transmitters on the basis of modulation, service, frequency and power.	1 st	Introduction about Practical Communication	
	2	Block diagram of AM transmitters and working of each stage	-	Systems	
	3	Block diagram and working principles of reactance FET and Armstrong FM transmitters			
2 ND	4	Revision & Problem Discussion	2 ND	To observe the	
2	•		2	waveforms at	
	5	AM/FM Radio Receivers,Principle and working with block diagram of super Heterodyne AM receiver		different stages of a AM	
	6	Function of each block and typical waveforms at input and output of each block.	-	uansmuer	
3 RD	7	Performance characteristics of a radio receiver: Sensitivity, selectivity, fidelity, S/N ratio, image rejection ratio and their measurement procedure.	3 RD	To observe the waveforms at different stages	
	8	ISI standards on radio receivers.		of a Radio Receiver	
	9	Selection criteria for intermediate frequency (IF).			
4 TH	10	Concepts of simple and delayed AGC.	4 TH	To align AM broadcast radio	
	11	Block diagram of an FM receiver,		receiver	
	12	Function of each block and waveforms at input and output, of different			
5 TH	13	Need for limiting and de-emphasis in FM reception	5 TH	To identify and study the	
	14	Block diagram of communication receivers, differences with respect to broadcast receivers.		various types of antennas used	
	15	Test of 1st and 2nd Unit	-	frequency	
6 TH	16	Antennas: Electromagnetic spectrum and its various ranges	6 TH	To identify and	
	17	VLF, LF, MF, HF, VHF, UHF, Microwave		various types of	
	18	Physical concept of radiation of electromagnetic energy from a dipole. Concept of Polarization of EM Waves		in different frequency	
7 TH	19	Definition and physical concepts of the terms used with antennas like point source, gain directivity	7 TH	To plot the radiation pattern of a directional and Omni directional antenna	
	20	Aperture, effective area, radiation pattern, beam width and radiation resistance,loss resistance			
	21	Types of antennas-brief description, characteristics and typical applications of half wave dipole, medium wave (mast) antenna			
8 TH	22	Types of antennas-brief description, characteristics and typical applications		To plot the variation of field	

		of half wave dipole		strength of a
	23	Medium wave (mast) antenna, folded dipole, patch, loop antenna,		radiated wave,
		yagi		with distance
		and ferrite, rod antenna (used in transistor receivers).		from a
	24	Brief description of broad-side and end fire arrays, their radiation		transmitting
		pattern		antenna
OTH	25	and applications (without analysis).	отн	TT 1 (1
9.11	25	Brief idea about Rhombic antenna and dish antenna.	9	lo plot the
	26	Revision	-	variation of field
	20			strength of a
ĺ	27	Propagation: Basic idea about different modes of wave		with distance
		propagation.		from a
				transmitting
10 TH	28	typical areas of application Ground wave propagation.	10 TH	Installation of
10	_0	oppion news of appion of order of the propagation	10	Dish Antenna
	29	characteristics of Ground wave propagation.		for best
	20		_	reception
	30	summer field equation for field strength		
11 TH	31	Space wave communication – line of sight propagation, standard	11 TH	To observe
		atmosphere,		waveforms at
	32	concept of effective earth radius range of space wave propagation		input and
		standard atmosphere.		output of ASK
	33	Duct propagation: sky wave propagation - ionosphere and its		and FSK
		layers.		modulator
12 th	34	Explanation of terms -virtual height, critical frequency, skips	12 TH	To observe
		distance		waveforms at
	35	maximum usable frequency, multiple hop Propagation.		input and
	26	Desizion & Ducklass Discussion		output of ASK
	30	Revision & Problem Discussion		and FSK
1.0TH	27		1.0TH	modulators
1311	37	Digital Modulation Techniques	13111	Revision & Viva
	38	Introduction of:PCM. DPCM	_	
	39	DELTA Modulation		
1 4 TH	40	Pasia block diagram and principle of working of the following ASK	1 4 TH	Davision & Viva
14	40	Basic block diagram and principle of working of the following ASK.	.14	Kevision & viva
Ì	41	Basic block diagram and principle of working of the following FSK		
	10		_	
	42	Basic block diagram and principle of working of the following PSK,		
1 <i>5</i> TH	42		1 <i>5</i> TH	D ' ' 0 X7'
15	43	Spread Spectrum Techniques & Frequency Hopping Technique	15	Revision & viva
	44	Revision & Problem Discussion	_	
	45	Test		
1			1	